

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:	Unassigned	Filing Date:	Filed Herewith
Confirmation No.:	Unassigned		
First Named Inventor:	Chia-Shun Hsiao		
Assignee:	Mosel Vitelic, Inc.		
Examiner:	Unassigned	Art Unit:	Unassigned
Attorney Docket No.:	M-15327 US		

San Jose, California
February 4, 2004

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR § 1.97(b)

Dear Sir:

Pursuant to 37 CFR § 1.56, § 1.97 and § 1.98, the documents listed on the accompanying form PTO-1449 are called to the attention of the Examiner for the above patent application. Copies of these documents are enclosed except for United States Patents and United States Published Applications.

Citation of these documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
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- or
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Application No. Unassigned

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Respectfully submitted,

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U.S. Department of Commerce, Patent and Trademark Office				Atty. Docket No.		Serial No.		
				M-15327 US		Unassigned		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Applicant(s)				
(Use several sheets if necessary)				Chia-Shun Hsiao et al.				
				Filing Date		Group		
				Herewith		Unassigned		
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA	2003/0067806	10 Apr. 2003	Tuan				
	AB	6,355,524	12 Mar. 2002	Tuan et al.				
	AC	6,541,324	1 Apr. 2003	Wang				
	AD							
	AE							
	AF							
	AG							
	AH							
	AI							
	AJ							
	AK							
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
	AQ	K. Naruke et al., "A New Flash-Erase EEPROM Cell with a Sidewall Select-Gate on Its Source Side", IEDM Technical Digest 1989, pages 603-606.						
	AR	United States Application No.: 10/402,698 filed March 28, 2003 by Chung et al.						
	AS	R. Mih et al., "0.18um Modular Triple Self-Aligned Embedded Split-Gate Flash Memory", 2000 Symposium on VLSI Technology, Digest of Technical Papers, pages 120-121.						
Examiner			Date Considered					
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.								